Serial No.: 10/043,468

IN THE SPECIFICATION:

Please replace paragraph [0001] with the following:

[0001] Cross-reference to Related Applications: This application is a continuation of application Serial No. 09/478,386, filed January 6, 2000, pending, which is a divisional of application Serial No. 09/304,368, filed May 4, 1999, now U.S. Patent 6,204,095 B1, issued March 20, 2001, which is a continuation of application Serial No. 09/056,124, filed April 6, 1998, now U.S. Patent 5,933,713, issued August 3, 1999.

Please replace the Abstract with the following:

A method for forming a semiconductor device includes forming a conductive bump on one or more of bond pads of a semiconductor substrate of a semiconductor wafer. A top or uppermost portion of each conductive bump is then planarized. The exposed portions of an active surface of the semiconductor wafer are filled with a layer of encapsulation material. The conductive bumps are reformed to their preplanarized shape and the semiconductor wafer is then diced to form singulated semiconductor dice. A preferred method of the invention also includes placing each singulated die in a mold to complete a second encapsulation step wherein a layer of encapsulation material is formed on the back surface or, alternatively, on the back and side surfaces of the semiconductor die in order to encapsulate the back, or the back and sides, of the semiconductor die.